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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR		ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/540,178	03/31/2000	Stephen R. Vogel		DIVA-244	2614
56015	7590 10/04/2006			EXAMINER	
PATTERSON & SHERIDAN, LLP/			MANNING, JOHN		
SEDNA PATENT SERVICES, LLC 595 SHREWSBURY AVENUE SUITE 100				ART UNIT	PAPER NUMBER
			2623		
SHREWSBU	IRY, NJ 07702			DATE MAILED: 10/04/2006	

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)				
	09/540,178	VOGEL ET AL.				
Office Action Summary	Examiner	Art Unit				
•	John Manning	2623				
The MAILING DATE of this communication app						
Period for Reply						
A SHORTENED STATUTORY PERIOD FOR REPLY WHICHEVER IS LONGER, FROM THE MAILING DA - Extensions of time may be available under the provisions of 37 CFR 1.13 after SIX (6) MONTHS from the mailing date of this communication. - If NO period for reply is specified above, the maximum statutory period w - Failure to reply within the set or extended period for reply will, by statute, Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	ATE OF THIS COMMUNICATION 36(a). In no event, however, may a reply be tim vill apply and will expire SIX (6) MONTHS from cause the application to become ABANDONE	lely filed the mailing date of this communication. (35 U.S.C. § 133).				
Status						
1) Responsive to communication(s) filed on	 '					
2a) ☐ This action is FINAL . 2b) ☒ This	This action is FINAL . 2b)⊠ This action is non-final.					
• • • • • • • • • • • • • • • • • • • •	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is					
closed in accordance with the practice under E	x parte Quayle, 1935 C.D. 11, 45	3 O.G. 213.				
Disposition of Claims						
4) Claim(s) 1-14 and 26 is/are pending in the apple 4a) Of the above claim(s) is/are withdraw 5) Claim(s) is/are allowed. 6) Claim(s) 1-14 and 26 is/are rejected. 7) Claim(s) is/are objected to. 8) Claim(s) are subject to restriction and/or	vn from consideration.					
Application Papers						
9) The specification is objected to by the Examiner 10) The drawing(s) filed on is/are: a) access Applicant may not request that any objection to the of Replacement drawing sheet(s) including the correction of the order	epted or b) objected to by the Eddrawing(s) be held in abeyance. See ion is required if the drawing(s) is obj	e 37 CFR 1.85(a). ected to. See 37 CFR 1.121(d).				
Priority under 35 U.S.C. § 119						
 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some * c) None of: 1. Certified copies of the priority documents have been received. 2. Certified copies of the priority documents have been received in Application No. 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received. 						
Attachment(s) 1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO/SB/08) Paper No(s)/Mail Date 6/13/05.	4) Interview Summary Paper No(s)/Mail Da 5) Notice of Informal Pa	te				

Art Unit: 2623.

DETAILED ACTION

Response to Arguments

1. In view of the appeal brief filed on 5/30/06, PROSECUTION IS HEREBY REOPENED. New grounds of rejection are set forth below.

To avoid abandonment of the application, appellant must exercise one of the following two options:

- (1) file a reply under 37 CFR 1.111 (if this Office action is non-final) or a reply under 37 CFR 1.113 (if this Office action is final); or,
- (2) initiate a new appeal by filing a notice of appeal under 37 CFR 41.31 followed by an appeal brief under 37 CFR 41.37. The previously paid notice of appeal fee and appeal brief fee can be applied to the new appeal. If, however, the appeal fees set forth in 37 CFR 41.20 have been increased since they were previously paid, then appellant must pay the difference between the increased fees and the amount previously paid.

A Supervisory Patent Examiner (SPE) has approved of reopening prosecution by signing below:

Claim Rejections - 35 USC § 103

- 2. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Art Unit: 2623

3. Claims 1-10 and 26 are rejected under 35 U.S.C. 103(a) as being unpatentable over Taylor et al (6,289,376) in further view of Imanaka (EP0854610) and Sawicz et al (5,889,775).

Regarding Claim 1, Taylor shows an apparatus having redundant provider equipment for improving fault tolerance comprising a server (fig. 2, items 208), comprising a plurality of server modules for storing content (fig.2, items 208-1, 208-2...208-n; col 3, lines 8-15), a switch coupled to each of the server modules at the head end for forwarding requested content from at least one of the plurality of server modules to the subscriber equipment (fig. 2, items 206-1 and 206-2; col 3, lines 46-50), and a plurality of head-end controllers coupled to each server module of the plurality of server modules via at least two signal paths (fig. 2, items 204-1 and 204-2; server controllers). Although Taylor shows at least two signal paths between the server modules (fig. 2), Taylor fails to specifically state that each communication between a head-end controller and a server module is coincidentally sent through the two signal paths. Imanaka shows that each communication between a server node another control module is coincidentally sent between two signal paths (fig. 1, col. 4 lines 10-20, transmission data from servers are concurrently sent to both communication lines 1 and 2). It would have been obvious to one of ordinary skill in the art at the time the invention was made to modify Taylor with the ability to coincidentally send two signals through two signal paths, as shown in Imanaka, so that the system would ensure signal reception as well as provide robust communications.

Art Unit: 2623

Regarding Claim 2, Taylor further shows that a plurality of subscriber equipment is capable of interfacing with the at least one head-end controller and server for receiving video on demand (col 2, lines 62-65).

Regarding Claim 3, Taylor shows at least two switches coupled between the at least one head-end controller and the server modules (see fig. 2, items 206-1 and 206-2).

Regarding Claim 4, Imanka shows the ability to send redundant messages across a network to a server module and the ability to discard messages that have been already received (col. 5 lines 2-25, identical reception data sent, either of the data which is received later is discarded). Furthermore, these signals must travel through switches, controllers, and servers since they are being sent through a large network (see fig. 1).

Regarding Claim 5, Imanaka shows disregarding either the initial or redundant message (col. 5 lines 2-25, identical reception data sent, either of the data which is received later is discarded).

Regarding Claim 6, Imanaka further shows that multiple acknowledgement data is routed from one server module to another controller (col. 7 lines 45-67, col. 8 lines 1-26, abnormality detection process, pieces of identification information and received data and pieces of corresponding reception time information are registered in system-A queue and system-B queue). Imanaka shows disregarding either the initial or redundant message (col. 5 lines 2-25,

Art Unit: 2623

identical reception data sent, either of the data which is received later is discarded).

Regarding Claim 7, Imanaka shows disregarding either the initial or redundant message (col. 5 lines 2-25, identical reception data sent, either of the data which is received later is discarded).

Regarding Claim 8, Taylor shows that the switch has a plurality of I/O ports coupled to the server modules and subscriber equipment for transferring the information (Fig. 2). Taylor further shows a least two switch controllers coupled to a head-end controller (fig. 2, item 220; MPEG cross bar switch) and the I/O ports. Cross bar switches have a characteristic matrix of switches between the inputs to the switch and the output of the switch. If the switch has M inputs and N outputs, then a crossbar has a matrix with M x N cross-points or places where there the "bars" "cross". Consequently, the cross bar switch is inherently a plurality of switches.

Taylor shows that the one of two switch controllers serves as a primary switch controller for routing the information between the I/O ports, and a second switch controller serves as a secondary switch controller for monitoring status of the I/O ports, whereby the secondary switch controller initiates a switchover in a an instance of a failure (col. 3 lines 46-50; fig. 2).

Regarding Claim 9, Taylor shows that the switch controller, or director, is coupled to the head-end controller, or server, via on of the switches and the second controller is coupled to the server via second switch (fig. 2).

Art Unit: 2623

Regarding Claim 10, all the limitations of the claim have been discussed with regards to Claim 9.

Regarding Claim 26, Taylor further shows a plurality of access controllers coupled to each head-end controller and said video switch (fig. 2), said access controllers for forwarding said requested content from said video switch to said subscriber equipment in response to a request for content from said subscriber equipment.

4. Claim 11 is rejected under 35 U.S.C. 103(a) as being unpatentable over Taylor et al (6,289,376) in further view of Imanaka (EP0854610), and Deitz et al (6,412,079).

Regarding Claim 11, although not specifically stated, it is nonetheless inherent that there must be a switch processor within the switch for processing control commands between the head-end controllers and switch controllers, and between the controllers and the I/O ports. This is inherent to all digitally controlled switches so that they may function properly in routing signals to a designated location.

Taylor fails to show a timer for periodically querying the operational status of the controllers. Deitz shows a 'pinging' system that periodically queries controllers to see if the controllers are operational (col. 7 lines 30-50). It would have been obvious to one of ordinary skill in the art at the time the invention was made to modify Taylor and Imanaka with the ability to query the switch controllers, as shown in Dietz, so that the system would know when a switch has failed and the status of an operational switch.

Art Unit: 2623

5. Claims 12-14 are rejected under 35 U.S.C. 103(a) as being unpatentable over Taylor et al (6,289,376) in further view of Imanaka (EP0854610), Deitz et al (6,412,079), and Miyamoto et al (5,845,061).

Regarding Claim 12, Deitz shows the ability to send periodic messages, or pinging, to controllers and the ability to indicate a problem when a certain time elapses. Furthermore, if these messages are periodic, it is inherent that there is some type of timer coupled to the sending apparatus (col. 6 lines 64-67, col., 7 lines 30-50). Also, although not specifically stated, it is inherent that the system must have a control registers to receive and store commands from the switch controller. Without this equipment, the system would not route signals properly or execute commands correctly. Taylor and Deitz fail to show a plurality of status registers. Miyamoto shows status registers that store the state of the system controllers to indicate whether or not that section of the system is operational or has failed (col. 11 lines 15-60). It would have been obvious to one of ordinary skill in the art at the time the invention was made to modify the system of Taylor, Imanaka, and Deitz with the status registers so that the system would be able to store the condition of the system for reference.

Dietz and Miyamoto also fail to show the use of a memory table for storing routing addresses. Official Notice is taken that it is well known and expected in the art to use tables to store the routing addresses of components in a network.

Therefore, it would have been obvious to one of ordinary skill in the art at the

Art Unit: 2623

time the invention was made to include memory tables so that the system would know the proper destination to send messages.

Regarding Claim 13, Miyamoto further shows pinging, or polling messages, to the system for information (col. 11 lines 1-14). Furthermore, it is inherent that there are control registers storing information about the system. Miyamoto further shows setting status registers with the appropriate information regarding the operational status of the system. Also, Miyamoto shows that if the status of the system is "occurrence of fault" then a back up system initiates (col. 11 lines 1-67, col. 12 lines 1-67). It is also inherent that the status of the system is stored as bits.

Regarding Claim 14, Miyamoto further shows polling messages to the system for information (col. 11 lines 1-14). Furthermore, it is inherent that there are control registers storing information about the system. Miyamoto further shows setting status registers with the appropriate information regarding the operational status of the system. Also, Miyamoto shows that if the status of the system is "occurrence of fault" then a back up system initiates (col. 11 lines 1-67, col. 12 lines 1-67). It is also inherent that the status of the system is stored as bits. Deitz also shows registering an error if an elapsed time has occurred between polling messages (col. 6 lines 64-67).

Application/Control Number: 09/540,178 Page 9

Art Unit: 2623

Conclusion

6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to John Manning whose telephone number is 571-272-7352. The examiner can normally be reached on M-F: 9:00 - 5:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, John W. Miller can be reached on 571-272-7353. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

JM September 27, 2006

> JOHN MILLER SUPERVISORY PATENT EXAMINER TECHNOLOGY CENTER 2600